

Europäisches Patentamt  
European Patent Office  
Office européen des brevets



(11) **EP 1 217 753 A1**

(12) **EUROPEAN PATENT APPLICATION**

(43) Date of publication:  
26.06.2002 Bulletin 2002/26

(51) Int Cl.7: **H04B 1/04**, **H03F 1/32**,  
**H04L 27/36**

(21) Application number: 01310323.9

(22) Date of filing: 11.12.2001

(84) Designated Contracting States:  
**AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU**  
**MC NL PT SE TR**  
Designated Extension States:  
**AL LT LV MK RO SI**

(72) Inventor: **Nielsen, Per Asbeck**  
2200 Copenhagen (DK)

(74) Representative: **Haws, Helen**  
**Nokia IPR Department,**  
**Nokia House,**  
**Summit Avenue**  
**Farnborough, Hampshire GU14 0NG (GB)**

(30) Priority: 15.12.2000 GB 0030684

(71) Applicant: **Nokia Corporation**  
02150 Espoo (FI)

(54) **Linear RF power amplifier and transmitter**

(57) A method for transforming digital amplitude as well as phase or frequency modulation information into analog signals for controlling analog amplifier circuitry, comprises:

- providing first and second digital signals representing phase angle;
- converting said signals into analog signals;
- mixing said analog signals with oscillator signals to obtain an analog, modulated signal;
- providing a third digital signal corresponding to am-

plitude;

- converting this signal into an analog signal; and
- feeding said analog, modulated signal and said analog signal to said analog amplifier circuitry.

A digital signal set consists of two digital signals representing phase angle in the form of a phase unit vector, and one digital signal representing amplitude.

A Radio Transmitter, use of the method, use of the digital signal set and use of the radio transmitter in mobile equipment, preferably a mobile, cellular telephone, is disclosed as well.

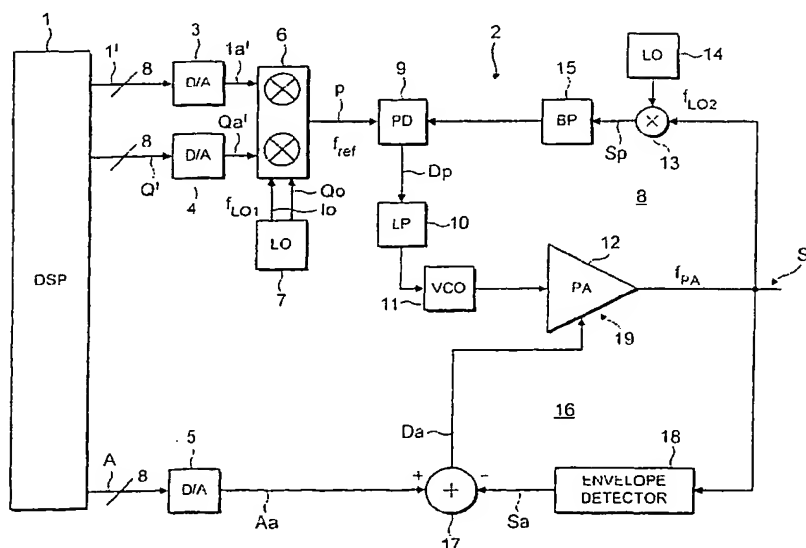


FIG. 1

## Description

[0001] The invention relates to radio transmitters for transmitting radio signals which varies in amplitude as well as in phase or frequency. To this end, the invention provides a set of digital signals; a radio transmitter; and a method for transforming modulation information from a digital signal processing system into analog signals suitable for controlling analog modulator and amplifier circuitry.

[0002] From the article: Asad A. Abidi: "Low-Power Radio-Frequency ICs for Portable Communications", Proceedings of the IEEE, Vol. 83 No. 4, April 1995, pp. 544-569, radio transmitters are known, e.g. in mobile telephones, where signals representing a desired transmitted radio signal are processed in a Digital Signal Processor and delivered from the processor to a transmitter radio frequency (RF) output unit as digital signals in quadrature form; cf. e.g. Fig. 13.

[0003] It is a disadvantage in the transmitters thus known that the non-linearities of the power amplifier distort the transmitted signal.

[0004] From the article: V. Petrovic: "Reduction of Spurious Emission from Radio Transmitters by means of Modulation Feedback", IEE Conference on Radio Spectrum Conservation Technique, September 1983, pp. 44-49, a Single-Sideband (SSB) Radio Transmitter is known, in which two output quadrature signals are generated from the output RF signal, and two input quadrature signals are generated from an input signal as well. The quadrature output signals are demodulated by two quadrature carrier signals generated digitally, and are fed back and compared with the input quadrature signals. Difference quadrature signals are amplified and modulated with said carrier signals and summed to form the output RF signal.

[0005] It is a disadvantage in this known transmitter that the input quadrature signals are generated from a complete, modulated input signal by means of a phase-shift circuit. The article mentions the difficulties experienced when designing a reliable wide-band 90° phase-shift network.

[0006] From the article: Hiroaki Kosugi *et al.*: "A High-Efficiency Linear Power Amplifier using an Envelope Feed-Back Method", Electronics and Communication in Japan, Part 2, Vol. 77, No. 3, 1994, pp. 50-57, a Power Amplifier is known, in which input signals are fed to a power amplifier in quadrature form, i.e. generated by a quadrature mixer not specified in detail. An envelope signal is derived mathematically from the input quadrature signals, and is used as independent variable in an envelope feed-back loop controlling the envelope of the power output signal. No phase feed-back is disclosed.

[0007] It is a disadvantage in this known amplifier that the phase distortion of the power amplifier is not controlled or overcome.

[0008] From the article: Leonard R. Kahn: "Single-Sideband Transmission by Envelope Elimination and

Restoration", Proceedings of the I.R.E., 1952, pp. 803-806, an SSB Transmitter is known, wherein the phase and the amplitude components of an input signal are separated and amplified independently before being combined in a final output stage.

[0009] It is a serious disadvantage in this transmitter that modulating the amplitude at a high power level tends to generate considerable amounts of spurious signals.

[0010] From the article: V. Petrovic & W. Gosling: "Polar-Loop Transmitter", Electronics Letters, 10 May 1979 Vol. 15 No. 10, pp. 286-288, a Polar Modulation Feedback Linearisation System for an SSB Transmitter is known, wherein the modulated output from a Radio Frequency Power Amplifier (RF PA) is being controlled by means of two closed loops, each having the same modulated Intermediate Frequency (IF) signal as the controlling variable. A first loop controls the Amplitude Modulation (AM) or the "envelope" of the RF output signal relative to the IF signal, and a second loop controls the Phase Modulation (PM) of the RF output signal relative to the IF signal. Polar representation of the RF signal vector is used in the article, the vector angle representing the signal phase and the vector magnitude representing the signal amplitude.

[0011] Thus, this polar modulation feedback linearisation system uses a fully modulated IF signal which is demodulated into an amplitude signal and a phase signal, respectively. It is a disadvantage that the analog control signals for the amplitude and phase loops thus have to be extracted from the IF signal.

[0012] It is an object of the invention to provide a digital signal set containing amplitude as well as phase or frequency modulation information, the signal set enabling transfer of this information from a digital domain to an analog domain in an efficient way, the signals being comparatively easily generated in the digital domain and at the same time being suited for conversion into an analog signal set in a way reducing efficiently the risk of "cross-talk" between the amplitude modulation and the phase or frequency modulation, and enabling a very efficient linearisation of a simple and energy-efficient analog output stage in a radio transmitter.

[0013] In a digital signal set containing amplitude as well as phase or frequency modulation information for compound modulating a carrier wave, this object is met in that the set comprises the following three digital signals:

- a first and a second digital signal representing a desired phase angle of the carrier wave; and
- a third digital signal representing a desired amplitude of the carrier wave.

[0014] Hereby, modulation information is provided which is suitable for driving particularly simple modulator circuits while obtaining high quality modulation with a minimum of cross-talk between the different types of

modulation, while said information is still simple to establish in a digital signal processing system.

[0015] Cross-talk between the amplitude and phase or frequency modulation, respectively, is primarily being avoided due to the fact that the amplitude modulation information and the phase or frequency modulation information, respectively, are separated from one another in the digital domain, such that the digital-to-analog conversion may be made on the separated modulation information signals rather than on a compound signal containing both types of modulation information. Retrieval of the two types of modulation information from one and the same compound analog signal of the prior art will not be possible with state-of-the-art analog circuitry without the use of very elaborate and expensive circuits.

[0016] It is preferred that said first and second digital signals correspond to each their component of a desired phase unit vector in a cartesian coordinate system.

[0017] Hereby, phase or frequency information is provided which is particularly suited for being transformed into analog phase or frequency information in a simple modulator circuit.

[0018] It is a further object of the invention to provide a method for amplitude and phase or frequency modulating an output signal from an amplifier circuit such as a radio transmitter output stage, the method permitting efficient and component-saving use of contemporary digital signal processing systems together with analog aerial output circuitry and at the same time eliminating the disadvantages of the prior art.

[0019] In a method for transforming modulation information from a digital signal processing system into analog signals suitable for controlling analog modulator and amplifier circuitry, the information comprising amplitude modulation information as well as phase or frequency modulation information for compound modulating a carrier wave, the object of the invention is met in that the method comprises the steps of:

- providing a first and a second digital signal representing a desired phase angle of the carrier wave;
- converting each of said first and second digital signals into corresponding first and second analog signals;
- mixing said first and second analog signals with first and second oscillator signals to obtain an analog, phase or frequency modulated signal;
- providing a third digital signal corresponding to the desired amplitude of the carrier wave;
- converting said third digital signal into a corresponding analog amplitude signal; and
- feeding said analog phase or frequency modulated signal and said analog amplitude signal to said analog modulator and amplifier circuitry.

[0020] Hereby, analog signals containing the desired analog as well as phase or frequency modulation information have been provided by means of very simple cir-

cuitry, the signals having very low levels of amplitude distortion and phase or frequency distortion, respectively.

[0021] The three digital signals mentioned may easily be provided by a digital signal processing system of the state of the art when adequately programmed, even if constraints are put to the first and the second digital signal as to representing a phase or frequency vector of unity magnitude, or at least of a constant magnitude.

[0022] It is generally preferred that each of the first and second digital signals, or each of said first and second analog signals, respectively, correspond to a respective component of a desired phase unit vector in a cartesian coordinate system.

[0023] In a cartesian coordinate system, an optimal discrimination or resolution of the phase information is obtained, relative to the discrimination or resolution of the components of the phase vector.

[0024] It is likewise preferred that said first and second oscillator signals are in quadrature.

[0025] By this measure, the local oscillator signals may be generated more easily with adequate precision, e.g. by frequency dividing a master oscillator square wave signal into two quadrature square wave signals, or by generating by means of a CR coupling and an RC coupling (resistance-capacitance couplings) a  $+45^\circ$  and a  $-45^\circ$  shifted sine wave from a master sine wave.

[0026] Preferably, said analog, phase or frequency modulated signal has a constant amplitude.

[0027] Hereby, any risk of amplitude noise from the phase or frequency modulated signal affecting or contaminating the amplitude modulation of the output signal is significantly reduced. In addition, a phase detector of a phase locked loop will perform better on a modulated signal having a constant amplitude.

[0028] It is a still further object of the invention to provide a radio transmitter meeting objects corresponding to the ones mentioned above with reference to the digital signal set and the method of the invention.

[0029] In a radio transmitter comprising a digital signal processing system as well as analog signal processing circuitry and an analog output stage, this object is met in that the radio transmitter further comprises:

- first and second digital-to-analog conversion circuits for converting first and second digital quadrature signals from the digital signal processing system into corresponding first and second analog quadrature signals;
- a first mixer circuit with an associated first local oscillator circuit for mixing said first and second analog quadrature signals with first and second quadrature signals from the local oscillator into a constant amplitude, phase or frequency modulated driver signal;
- frequency converter circuitry for converting the driver signal to a desired output frequency band;
- a power amplifier for amplifying the converted sig-

- a third digital-to-analog conversion circuit for converting a digital amplitude signal from the digital signal processing system into a corresponding analog amplitude signal; and
- an amplitude modulating circuit for modulating the amplitude of the transmitter output signal in dependency of the analog amplitude signal.

**[0030]** Hereby, simple modulator circuitry is obtained, in which the analog output stage may be supplied with digital modulation signals which are easily generated within the digital signal processor, while enabling modulation of the transmitter output signal with very low phase or frequency distortion.

**[0031]** It is generally preferred that said frequency converter circuitry together with said power amplifier forms part of a phase locked loop.

**[0032]** The advantages known *per se* of the phase locked loop enables full benefit to be had of the advantageous phase modulation circuitry of the invention; in particular, it will enable a very high phase linearity of the output power amplifier, which will tend to minimise any phase distortion arising from imperfections in the power amplifier, or from amplitude modulating the power amplifier.

**[0033]** It is further preferred that said frequency converter circuitry comprises in a main signal path a voltage controlled oscillator controlling a power amplifier, and in a feed-back path a divider circuit, or a second mixer circuit with an associated second local oscillator circuit.

**[0034]** This embodiment provides for a further simple and stable phase locked loop.

**[0035]** In a preferred embodiment, the amplitude modulating circuit comprises a closed amplitude control loop.

**[0036]** Hereby, the amplitude linearity of the analog circuitry will be substantially improved.

**[0037]** In a particularly preferred embodiment, said power amplifier forms part of said amplitude control loop.

**[0038]** In this embodiment, the resulting amplitude linearity of the output stage will generally depend on the characteristics of the amplitude control loop rather than the characteristics of the power amplifier. This enables use of power amplifiers with less ideal amplitude linearity, and among such power amplifiers are particularly power efficient amplifiers. The use of such power efficient amplifiers are generally advantageous, and particularly so in battery powered equipment such as mobile equipment.

**[0039]** "Cross-talk" from the amplitude modulation to the phase or frequency modulation (or: phase or frequency distortion, respectively, generated by the amplitude modulation) in amplifiers processing compound modulated signals will always be present to some small extent, due to the very fact that the shift of the amplifier gain introduced by the shift of the modulated amplitude

will in itself affect the momentary phase or frequency, respectively, of the signal.

**[0040]** Substantial cross-talk from the amplitude modulation to the phase or frequency which will normally be encountered when using a power amplifier with a poor amplitude linearity for amplifying signals that are both amplitude and phase or frequency modulated, is however avoided in the embodiments of the invention with both an amplitude feedback loop and a phase locked loop, because the phase distortion generated from the amplitude modulated signal by the amplitude linearity defects of the power amplifier are cancelled by the phase linearity feedback of the phase locked loop.

**[0041]** This is so because the phase distortion is generated inside of the phase locked loop, and thus in a part of the circuit which is controlled by the phase locked loop.

**[0042]** Further, it is an object of the invention to provide an advantageous use of the digital signal set of the invention in a mobile radio transmitter, preferably a mobile radio transmitter in a cellular telephone.

**[0043]** This use will make possible a lower component count in the modulator circuitry of a mobile radio transmitter, thus providing for lower weight, lower price and lower power consumption.

**[0044]** Finally, it is an object of the invention to provide an advantageous use of the radio transmitter of the invention in mobile equipment, preferably a mobile, cellular telephone.

**[0045]** This use will make possible a lower component count and a higher power efficiency in the output stage with similar or better modulation quality, thus providing for lower weight, lower price, lower power consumption and higher signal quality.

**[0046]** Below, the invention will be explained in more detail by means of embodiment examples and with reference to the drawings, in which corresponding reference designations refer to same or corresponding items, reference letters generally refer to signals and reference numerals generally refer to elements, and in which:

Fig. 1 schematically shows part of the circuitry in a radio transmitter according to the invention;

Fig. 2 schematically shows the circuitry in Fig. 1, with a loop amplifier in the amplitude feedback loop, and a power amplifier comprising a variable gain amplifier;

Fig. 3 schematically shows a loop amplifier for use in the circuitry in Fig. 2; and

Fig. 4 is a graph showing the RMS voltage error of the output signal from the circuit of Fig. 1 plotted as a function of the input amplitude level, with and without amplitude feedback loop gain control.

[0047] In Fig. 1, a digital signal processor 1 produces digital signals I', Q', A containing modulation information. The digital signal processor communicates in a way not shown with other circuitry in the radio transmitter, such as signal providing circuitry.

[0048] In the embodiment shown, the digital signals I', Q', A are used for controlling an analog output stage 2. The analog output stage 2 produces an output signal S which is fed to an aerial.

[0049] According to one embodiment of the invention, the digital signals I' and Q' represent the cartesian coordinates of a unit phase vector of the desired output signal S. In the embodiment shown, this is accomplished by the signal I' having a value proportional to  $\cos \varphi(t)$  and the signal Q' having a value proportional to  $\sin \varphi(t)$ ,  $\varphi(t)$  being the momentary value of a desired phase shift of the phase modulated output signal S.

[0050] In this way, the value of  $\varphi(t)$  designates the momentary phase value which is to be added to the phase of the carrier wave of the output signal S, in order to obtain the desired phase modulation of this signal. It is evident that the phase shift representation provided by the I' and Q' signals constitutes unambiguous information on the desired phase modulation of the output signal S, as long as the phase shift is kept within the interval of  $-180^\circ < \varphi(t) < 180^\circ$ .

[0051] Phase and/or frequency modulation schemes are known for providing unambiguous phase information beyond the limits of this interval.

[0052] In the embodiment shown, the digital signal A represents the amplitude value of the output signal S.

[0053] Thus, the complete output signal S may be expressed as  $S(t) = A(t)(\text{Re}[(I'(t) + jQ'(t))e^{j\omega_c t}])$ ; j being the imaginary unit, and  $\omega_c$  being the angular frequency of the carrier wave.

[0054] The digital signals I', Q', A are preferably represented as n-bit words with the bits transmitted in parallel. In Fig. 1, said digital signals are represented as each their 8-bit word, transmitted on 8-wire lines. This does not, however, implicate that 8-bit words are required or preferred in any specific application. For example, words of 8 to 12 bits might be preferred for use in e.g. cellular telephone equipment.

[0055] Each 8-wire line in Fig. 1 runs to a digital-to-analog converter 3-5, where the respective digital signals are converted to analog voltage signals I'a, Q'a, Aa.

[0056] The two analog voltage signals I'a, Q'a are carried to a quadrature mixer 6 where these two signals are mixed with two signals I<sub>o</sub>, Q<sub>o</sub> from a local oscillator 7, operating at a frequency  $f_{LO1}$ . The two signals I<sub>o</sub>, Q<sub>o</sub> are in quadrature, i.e. phase shifted  $90^\circ$  respective to one another.

[0057] Such local oscillator signals may be generated easily with adequate precision, e.g. by frequency dividing a master oscillator square wave signal into two quadrature square wave signals, or by generating by means of a CR coupling and an RC coupling (resistance-capacitance couplings) a  $+45^\circ$  and a  $-45^\circ$  phase shifted

sine wave from a master sine wave.

[0058] The output from the quadrature mixer 6 is a single analog phase signal P, having a fixed carrier frequency  $f_{ref}$  and being phase modulated with a phase shift corresponding to the phase of the virtual phase vector (I', Q').

[0059] In any case, the signal P is a constant magnitude signal (preferably a constant voltage signal) and thus free from any amplitude information and amplitude noise. The signal P may preferably be amplitude limited so as to ensure a constant magnitude, and reduce any possible amplitude noise.

[0060] The analog phase signal P is now used as reference input to a phase locked loop 8. The elements of this loop comprise:

- a phase detector 9;
- a low-pass filter 10;
- a voltage controlled oscillator 11;
- a power amplifier 12;
- a feedback (or: down-conversion) mixer 13;
- a local oscillator 14; and
- a band-pass filter 15.

The functioning of the phase locked loop 8 is preferably as follows:

[0061] The output signal S having the frequency  $f_{pA}$  is carried to a mixer 13 where it is mixed with a local oscillator signal of frequency  $f_{LO2}$  from a local oscillator 14, in order to obtain a signal  $S_p$  having the phase shift or offset of the output signal S, but at the frequency of the analog phase signal P. The signal  $S_p$  and the analog phase signal P are led to a phase detector 9, detecting the phase difference between the signals P and  $S_p$ . A low-pass filter or preferably a band-pass filter 15 is coupled between the mixer 13 and the phase detector 9.

[0062] A phase difference signal  $D_p$  is carried via a low-pass filter 10 to a voltage controlled oscillator 11.

This oscillator oscillates at a centre frequency corresponding to the centre frequency of the desired output signal, and its output signal is used as input signal to a power amplifier 12 providing the desired output signal S.

[0063] Thus, the voltage controlled oscillator 11 acts as a frequency converter circuit in that it provides the desired phase information (contained in the phase signal P) at a desired carrier frequency (i.e., the centre frequency of the voltage controlled oscillator 11).

[0064] The phase locked loop 8 is now closed, in that the signal S is carried to the mixer 13 as previously mentioned.

[0065] As the actual frequency of the voltage controlled oscillator 11 and hence the power amplifier 12 is now controllable by the phase difference signal  $D_p$  carried to the voltage controlled oscillator 11 via the low-pass filter 10, any phase difference between the analog phase signal P and the output signal S is minimised by the feedback in the phase locked loop 8.

[0066] Thus, the phase of the output signal S is the same as the phase of the analog phase signal P.

[0067] In another embodiment (not shown) of the phase loop of the invention, the feedback mixer 13, the local oscillator 14 and the band-pass filter 15 are replaced by a divider circuit, dividing down the output signal S from the frequency  $f_{pA}$  to the frequency  $f_{ref}$  of the quadrature mixer 6 and the phase detector 9. This will complete and close the phase lock loop as well, but the resulting phase shift of the output signal S will be multiplied by the factor  $f_{pA}/f_{ref}$  as compared to the signal P.

[0068] In this embodiment, therefore, the phase shift defined by the digital signals I', Q' must be reduced by the factor  $f_{pA}/f_{ref}$  as compared to the desired phase shift of the output signal S.

[0069] Returning to Fig.1, the analog voltage signal Aa is used as reference input to an amplitude feedback loop 16. The elements of this loop comprise:

- an analog difference element 17;
- the power amplifier 12, provided with a gain control input 19; and
- an amplitude detector (or AM demodulator) 18.

The functioning of the amplitude feedback loop 16 is preferably as follows:

[0070] The output signal S is led to the amplitude detector 18. The output signal Sa from said detector 18 is now corresponding to the momentary amplitude of the output signal S.

[0071] The analog amplitude signal Aa and the detected amplitude Sa is compared in the analog difference element 17, and the difference Da is carried to the gain control input 19 of the power amplifier 12.

[0072] The amplitude feedback loop 16 is now closed, in that the signal S is carried to the amplitude detector 18 as mentioned above.

[0073] As the actual amplitude of the output signal S is now controllable by the amplitude difference signal Da, any amplitude disproportionality between the analog amplitude signal Aa and the output signal S is minimised by the feedback in the amplitude feedback loop 16.

[0074] Thus, the amplitude of the output signal S is controlled to be proportional to the amplitude of the analog amplitude signal Aa. In this way, the amplitude feedback loop 16 acts as an amplitude control loop or an amplitude modulating circuit, providing a desired modulated amplitude of the output signal S.

[0075] The output signal S is carried to an aerial by means not shown; these may include antenna tuning components or circuits.

[0076] By the measures thus explained, the use of comparatively simple analog modulator circuits is made possible, partly owing to a greater part of the processing of the modulating signals being carried out in the digital domain, preferably in the digital signal processor 1.

[0077] Further, phase and amplitude modulation in-

formation are kept entirely separate within the analog domain, except for the power amplifier 12, thus minimising cross-talk between the phase and amplitude modulation from the digital signal processor 1 to the power amplifier 12.

[0078] By the very principle of having two feedback loops closed around the power amplifier 12, that is, the phase locked loop 8 and the amplitude feedback loop 16, amplitude distortion of the signals in the phase loop will not affect the output signal S significantly, and the phase variations of the output signal S will not affect the functioning of the amplitude feedback loop 16.

[0079] The provision of the digital signals I', Q', A containing the phase and amplitude modulation information from an existing digital signal processing system is still simple, being mostly dependent on appropriate software.

[0080] An important advantage of the set of said three digital signals according to the invention is that the signals will be more linear and less noisy than corresponding analog signals would be, owing to the greater precision and less noise susceptibility of the circuits of the digital signal processor as compared to analog circuitry. Further, the kind of signal processing involved is handled more power efficient and more precise by the digital signal processor than by analog circuits.

[0081] If the necessary modulation information is available in the digital signal processor in the form of an I signal and a Q signal, that is, signals representing an actual phase-and-amplitude vector of the output signal S, the following transformation or calculations will provide the I' and the Q' signals of the present invention:

[0082] I and Q are the complex coordinates of a vector, whose angle with the real axis represents the desired phase shift and whose magnitude represents the desired amplitude of the output signal. Thus,  $I(t) = A(t) \cos \phi(t)$  and  $Q(t) = A(t) \sin \phi(t)$ ; A(t) being the momentary amplitude and  $\phi(t)$  being the momentary value of a desired phase shift of the phase modulated output signal S.

[0083] From these signals I(t) and Q(t), the signal set of the invention may now be obtained by the following calculations:

$$A(t) = (I(t)^2 + Q(t)^2)^{-1/2}$$

$$I'(t) = I(t)/A(t)$$

$$Q'(t) = Q(t)/A(t)$$

[0084] These calculations are easily and efficiently performed by a digital signal processor of the state of the art, and will not occupy significant amounts of its signal processing power.

[0085] The power amplifier 12 having the gain control

input 19 need not be provided as a one-stage amplifier, as it is indicated in Fig. 1.

[0086] Fig. 2 shows a preferred embodiment of the circuit in Fig. 1. In Fig. 2, the power amplifier 12 is provided as a two-stage circuit comprising a variable gain amplifier 24 acting as a pre-amplifier as well as a regular power amplifier 25. According to the invention, both these amplifiers 24-25 are preferably equipped with gain control inputs 26 and 27, respectively, as shown in Fig. 2.

[0087] In this way, the utilised gain range per stage might be made narrower, permitting use of simpler amplifiers 24-25 with narrower gain ranges. For example, in certain power amplifier stages 25 having otherwise desirable characteristics, the gain tend to drop to near zero for very small or very large signal amplitudes.

[0088] It would require rather much from the amplitude feedback loop 16 to compensate for such high variations in the gain of the power amplifier 12. By dividing the power amplifier 12 into two amplifier stages 24-25 as shown in Fig. 2, each of these stages need only be controlled through a gain range of, in principle, the square root of the desired, total gain range of the whole power amplifier 12. Dividing the power amplifier 12 in more than two stages will of course be within the scope of the present invention as well.

[0089] In Fig. 2, the gain of the amplitude feedback loop 16 is made controllable by inserting a loop amplifier 20 in the feedback path of the amplitude feedback loop 16.

The amplitude linearity of the power amplifier (from the analog amplitude signal  $A_a$  to the output signal amplitude  $S$ ) and the stability of the amplitude feedback loop 16 are dependent on the loop gain of this loop. The higher the loop gain in the amplitude feedback loop, the better the amplitude linearity and the poorer the loop stability.

[0090] In general, the loop gain has to be kept below a certain fraction of that loop gain which causes instability in the loop. The difference between said fraction and the loop gain value causing instability is often referred to as the stability margin. In order to obtain "clean" and well controlled output signals, the stability margin has to be kept above a certain value.

[0091] Now, according to the invention, the gain of the loop amplifier 20 is controlled in dependency of the amplitude of the output signal  $S$  so as to obtain a loop gain in the amplitude feedback loop 16 of substantially constant value.

[0092] Preferably, the loop amplifier 20 has its gain controllable by means of a gain control signal  $G$  which is present on a gain control input 21 of said amplifier 20.

[0093] The loop amplifier 20 now amplifies the amplitude difference signal  $D_a$  by a factor corresponding to the gain control signal  $G$ , into a power amplifier gain control signal  $G_a$  to be coupled to the gain control input 19 of the power amplifier 12.

[0094] When the gain characteristics of the power amplifier 12 and of the loop amplifier 20 are known, it will

be possible to determine for each level of the desired amplitude of the output signal  $S$ , a value of the gain control signal  $G$  which will render the loop gain substantially constant at a desired level.

[0095] This however defines a relationship between the desired amplitude of the output signal  $S$  and a corresponding value of the gain control signal  $G$ . Thus, this relationship may according to the invention be implemented within the digital signal processor 1 so that said processor supplies a gain control signal  $G$  which is constantly adjusted to obtain a loop gain of substantially constant value as well as a satisfactory stability margin, even if the value of the output signal amplitude and hence the gain of the power amplifier 12 is varying considerably.

[0096] In the embodiment in Fig. 2, the gain control signal is by way of example provided by the digital signal processor 1 in the form of an 8-bit word.

[0097] Technically simple, power efficient and economically profitable power amplifiers of the state of the art often have amplitude gains which are varying highly with the amplitude level of the amplifier output, and amplitude modulated output signals of high quality from such amplifiers will not be obtainable without appropriate countermeasures.

[0098] On the other hand, the prospective savings in cost as well as power consumption using such amplifiers might be considerable.

[0099] By the measures of the invention thus described, the amplitude linearity of the power amplifier 12 is controlled efficiently by the amplitude feedback loop 16. This will permit use of a power amplifier of quite poor amplitude linearity, provided it has a possibility of real-time gain control.

[0100] In practice, experiments and modelling have proved that power amplifiers 12 of quite poor amplitude linearity might be used in the circuit of Fig. 2, with very satisfactory results indeed.

[0101] Further, experiments have indicated that the accuracy of the loop gain value need not be very high. Thus, according to the invention, the relationship between the desired amplitude of the output signal  $S$  and a corresponding value of the gain control signal  $G$  may be implemented in the form of an internal look-up table in the digital signal processor, and in order to save memory space, the look-up table may be quantified in that for a certain value interval of the independent variable (here: the desired amplitude of the output signal  $S$ ), one and only one value of the dependent variable (here: the value of the gain control signal  $G$ ) is put out from the table.

[0102] In addition, to further simplify matters, according to the invention, the contents of said look-up table may be determined from measurements on one or few sample power amplifiers of the type to be used in the application in question. Experiments have shown that the deviations as to amplitude linearity characteristics from sample to sample of power amplifiers tend to be

negligible in the present context.

[0103] In Fig. 3, a simple and advantageous implementation of an inverting, controllable gain amplifier suitable for use as the loop amplifier 20, is shown. For the sake of simplicity, it is assumed in Fig. 3 that the gain control signal G is a 3-bit word. Each of the three bits control one of the three switches shown, which engage each their feedback resistor of an inverting amplifier 22. By closing appropriate switches, a desired resulting resistance value of the feedback resistor network 23 and hence a desired gain of the amplifier 20 is obtained.

[0104] In fact, the loop amplifier 20 in Fig. 3 has a digital control input which in the embodiment shown will receive a digital signal in the form of a 3-bit word G(t) as its control signal. This digital signal might preferably be supplied directly by the digital signal processor 1, as indicated in Fig. 2.

[0105] As an alternative (not shown) to the digitally controllable gain amplifier in Fig. 3, an analog multiplier (or: Gilbert-mixer) may be utilised for the loop amplifier 20. Such a multiplier will have its analog output voltage proportional to the product of the values of its two analog input voltages. Then, the resulting gain from a first input to the output will be proportional to the analog voltage on a second input.

[0106] This alternative, of course, will necessitate a digital-to-analog conversion of the gain control signal G into an analog signal to be coupled to said second input of the analog multiplier. The amplitude difference signal Da is then coupled to said first input, and the power amplifier gain control signal Ga taken from said output of the analog multiplier.

[0107] In Fig. 4, results from a model simulation of a power amplifier 12 in an amplifier according to the invention are plotted in a graph. In the simulation in question, an amplitude feedback loop 16 was simulated, with its loop gain controlled by means of a loop amplifier 20, and without such loop gain control, respectively.

[0108] In the simulation, the gain characteristic of the power amplifier 12 was chosen so that the gain was set to approach zero for very small and for very large signal amplitudes, due i.a. to compression and turn-off phenomena. This will correspond to the conditions when using one of several common types of simple and power efficient power amplifiers 12.

[0109] The value of the gain control signal G was quantified to 4 bits, giving a minimum value of 1 and a maximum value of 5. The maximum DC loop gain was 20.

[0110] A test signal of a certain duration, amplitude as well as phase modulated, was defined in the form of a time-dependent (I,Q)-vector. This signal was split into a time-dependent (I',Q'A)-signal, and applied to said simulated circuit. Nine simulations were made, with the test signal at nine different amplitude maximum levels.

[0111] The RMS amplitude error voltages as functions of the test signal amplitude level is shown in the graph in Fig. 4. In Fig. 4, the abscissa is the relative max-

imum amplitude level of the test signals, and the ordinate is the corresponding RMS amplitude errors.

[0112] The curve "X" shows the RMS amplitude error without loop gain control, and the curve "□" shows the RMS amplitude error with loop gain control ("Gain Compensation") in the amplitude loop.

[0113] At two amplitude levels, the difference between the RMS error voltages without and with gain control respectively, is indicated. At approximately 0.15 V maximum input amplitude level, this difference amounts to 6% of the maximum input amplitude level, and at approximately 0.70 V maximum input amplitude level, the difference amounts to 0.6% of the maximum input amplitude level.

[0114] A considerable improvement is thus observed for all maximum amplitude levels, even though both systems have the same maximum loop gain and stability margin. For low amplitudes, said improvement of more than 6% RMS is observed.

[0115] The aspects and the embodiments of the invention have been explained above in general terms to illustrate the general utility of the invention in areas where accurate analog power amplification of digitally processed signals is needed. However, the use of the invention in the area of cellular telephony will be particularly advantageous, in particular for the following reasons.

[0116] In mobile telephony, the need for information throughput is steadily increasing. The use of combined amplitude and phase modulation will enable a higher information transfer per channel as compared to systems with only phase modulation such as e.g. the original GSM system. Such higher data rates are needed in coming generations of cellular telephony, as well as in planned extensions of the GSM system.

[0117] The present invention will provide systems, methods, circuits and uses which will enable a higher information data rates in cellular telephone channels.

[0118] As cellular telephones are highly price sensitive products due to the heavy competition on the market, it is essential that the electronic circuits in the telephones be manufactured as cost effectively as possible. This requires a low component count and few analog circuits which might require trimming or adjustment. These objectives are met by the invention, as has been explained above.

[0119] Also, power consumption of a cellular telephone is a specific competition parameter, because low power consumption will reduce battery weight and/or increase service time of the telephone. The invention contributes to a low power consumption of a cellular telephone.

[0120] These facts make the use of the different aspects of the invention explained above very advantageous in cellular telephones as well as in other mobile radio transmitter equipment.

[0121] Even if reference is made above merely to phase modulation and to a phase locked loop, nothing

will prevent the method, the digital signal set, the radio transmitter or the uses according to the invention to be utilised with use of frequency modulation instead of phase modulation, and a frequency linearising feedback loop instead of a phase locked loop.

## Claims

1. A Method for transforming modulation information from a digital signal processing system into analog signals suitable for controlling analog modulator and amplifier circuitry, the information comprising amplitude modulation information as well as phase or frequency modulation information for compound modulating a carrier wave, the method comprising the steps of:
  - providing a first and a second digital signal representing a desired phase angle of the carrier wave;
  - converting each of said first and second digital signals into corresponding first and second analog signals;
  - mixing said first and second analog signals with first and second oscillator signals to obtain an analog, phase or frequency modulated signal;
  - providing a third digital signal corresponding to the desired amplitude of the carrier wave;
  - converting said third digital signal into a corresponding analog amplitude signal; and
  - feeding said analog phase or frequency modulated signal and said analog amplitude signal to said analog modulator and amplifier circuitry.
2. A Method according to claim 1, wherein each of said first and second digital signals corresponds to a respective component of a desired phase unit vector in a cartesian coordinate system.
3. A Method according to claim 1, wherein each of said first and second analog signals corresponds to a respective component of a desired phase unit vector in a cartesian coordinate system.
4. A Method according to claim 2 or 3, wherein said first and second oscillator signals are in quadrature.
5. A Method according to any of the preceding claims, wherein said analog, phase or frequency modulated signal has a constant amplitude.
6. A Method for transforming modulation information from a digital signal processing system into analog signals suitable for controlling analog modulator and amplifier circuitry substantially as hereinbefore described with reference to and as illustrated in the accompanying drawings.
7. A digital signal set containing amplitude as well as phase or frequency modulation information for compound modulating a carrier wave, the set comprising the following three digital signals:
  - a first and a second digital signal representing a desired phase angle of the carrier wave; and
  - a third digital signal representing a desired amplitude of the carrier wave.
8. A digital signal set according to claim 7, wherein said first and second digital signals correspond to each their component of a desired phase unit vector in a cartesian coordinate system.
9. A digital signal set containing amplitude as well as phase or frequency modulation information substantially as hereinbefore described.
10. A Radio Transmitter comprising:
  - a digital signal processing system as well as analog signal processing circuitry and an analog output stage;
  - first and second digital-to-analog conversion circuits for converting first and second digital quadrature signals from the digital signal processing system into corresponding first and second analog quadrature signals;
  - a first mixer circuit with an associated first local oscillator circuit for mixing said first and second analog quadrature signals with first and second quadrature signals from the local oscillator into a constant amplitude, phase or frequency modulated driver signal;
  - frequency converter circuitry for converting the driver signal to a desired output frequency band;
  - a power amplifier for amplifying the converted signal into a transmitter output signal;
  - a third digital-to-analog conversion circuit for converting a digital amplitude signal from the digital signal processing system into a corresponding analog amplitude signal; and
  - an amplitude modulating circuit for modulating the amplitude of the transmitter output signal in dependency of the analog amplitude signal.
11. A Radio Transmitter according to claim 10, wherein said frequency converter circuitry together with said power amplifier forms form part of a phase locked loop.
12. A Radio Transmitter according to claim 11, wherein said frequency converter circuitry comprises in a main signal path a voltage controlled oscillator controlling a power amplifier, and in a feed-back path a divider circuit.

13. A Radio Transmitter according to claim 11, wherein said frequency converter circuitry comprises in a main signal path a voltage controlled oscillator controlling a power amplifier, and in a feed-back path a second mixer circuit with an associated second local oscillator circuit. 5
14. A Radio Transmitter according to any of the claims 10-13, wherein said amplitude modulating circuit comprises a closed amplitude control loop. 10
15. A Radio Transmitter according to claim 14, wherein said power amplifier forms part of said amplitude control loop. 15
16. A Radio Transmitter substantially as hereinbefore described with reference to and as illustrated in the accompanying drawings.
17. The use of the method of claim 1 in a mobile radio transmitter, preferably a mobile radio transmitter in a cellular telephone. 20
18. The use of the digital signal set of claim 7 in a mobile radio transmitter, preferably a mobile radio transmitter in a cellular telephone. 25
19. The use of the radio transmitter of claim 10 in mobile equipment, preferably a mobile, cellular telephone. 30

35

40

45

50

55

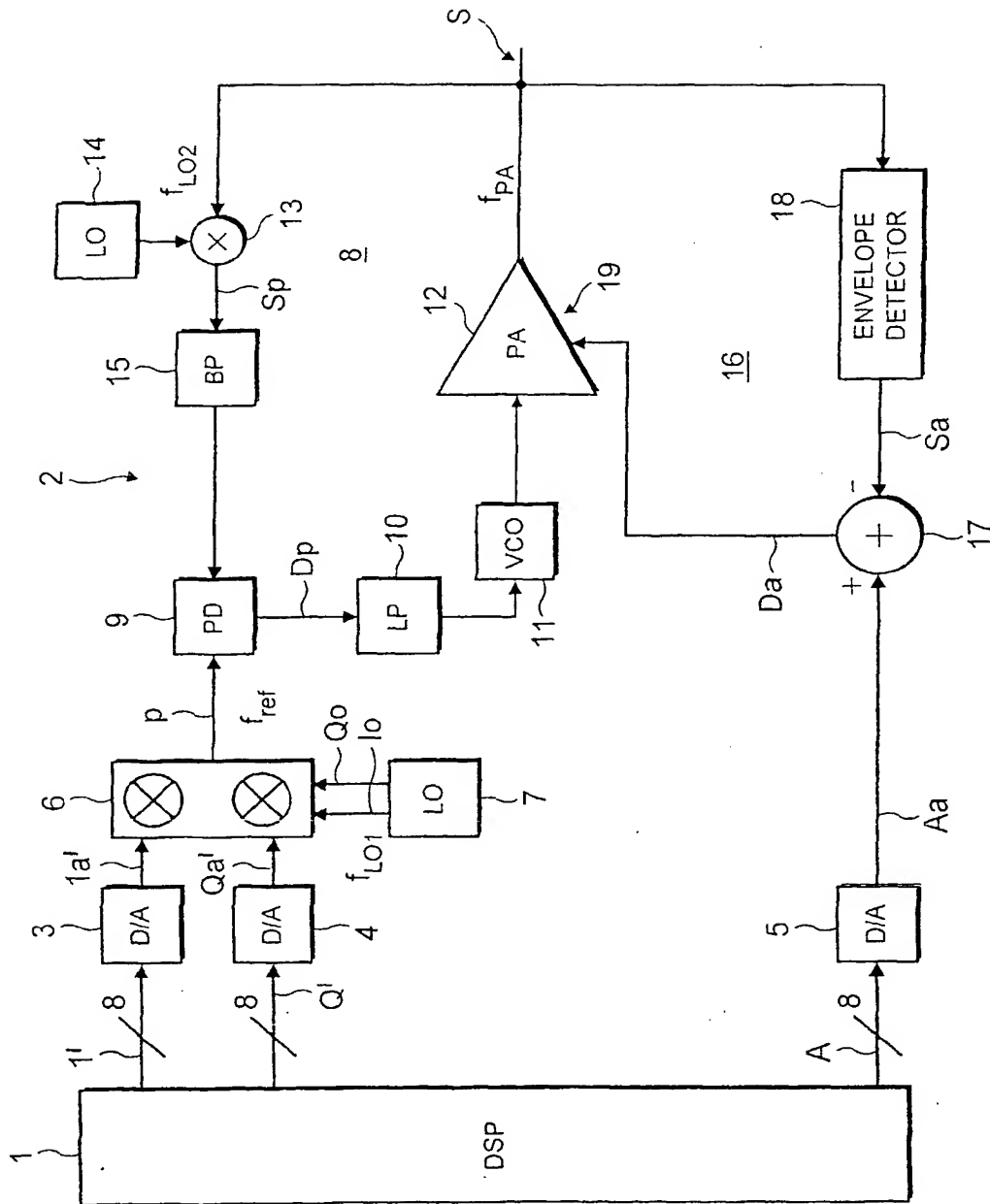


FIG. 1

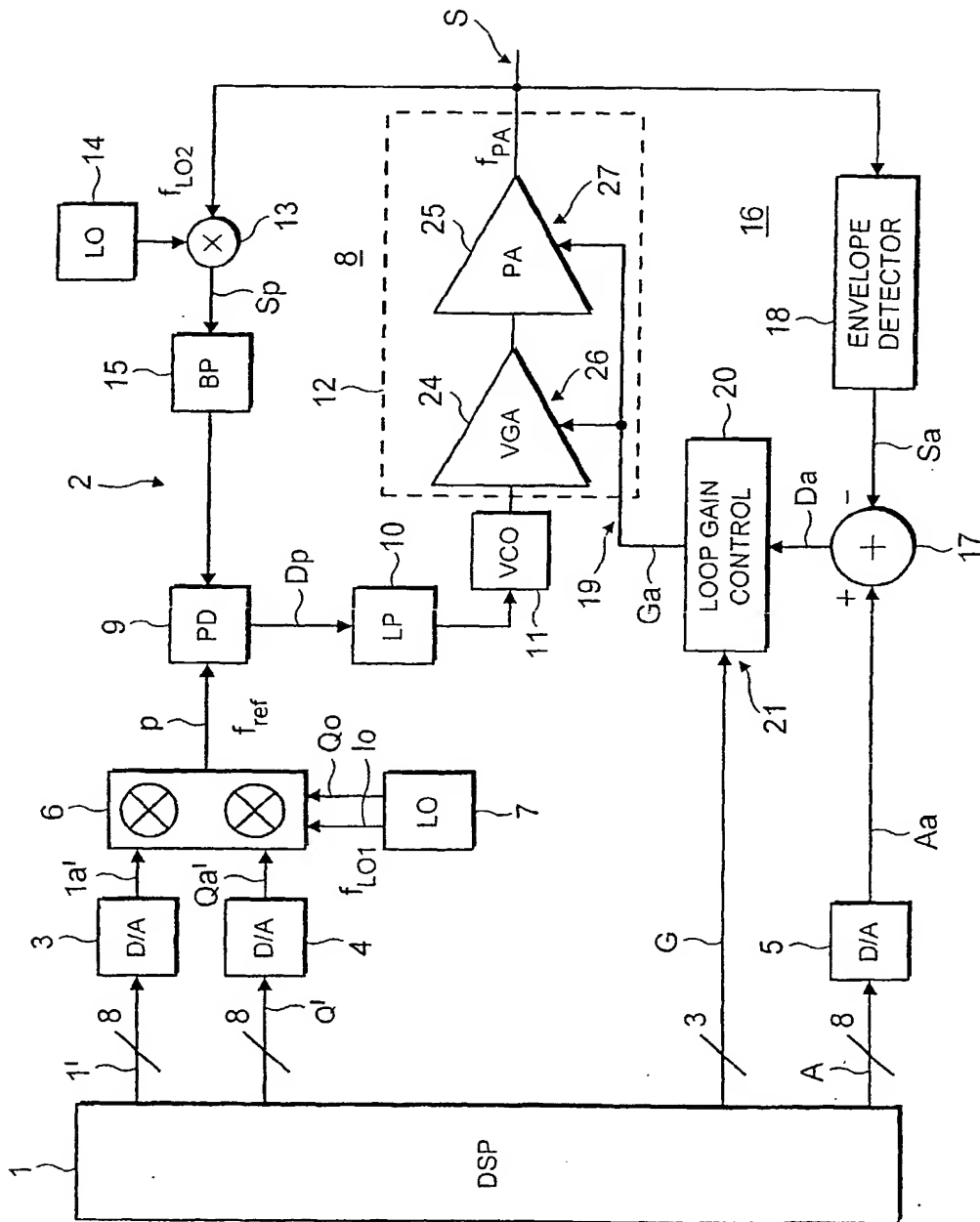


FIG. 2

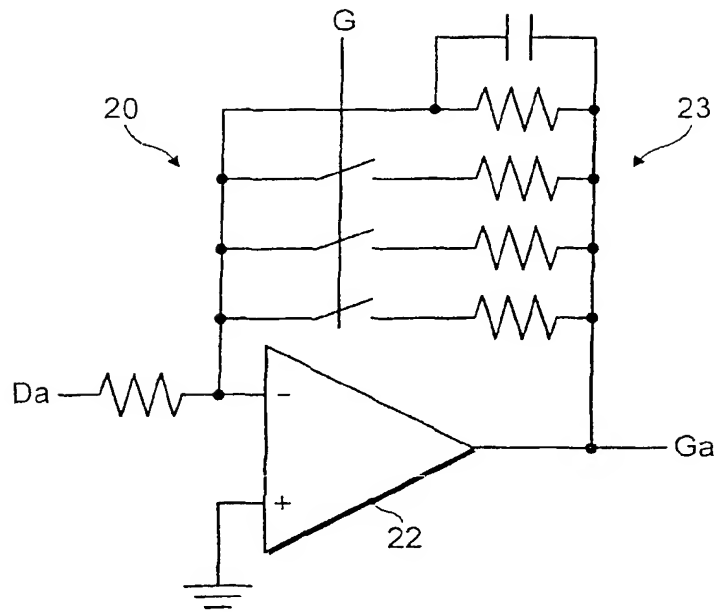


FIG. 3

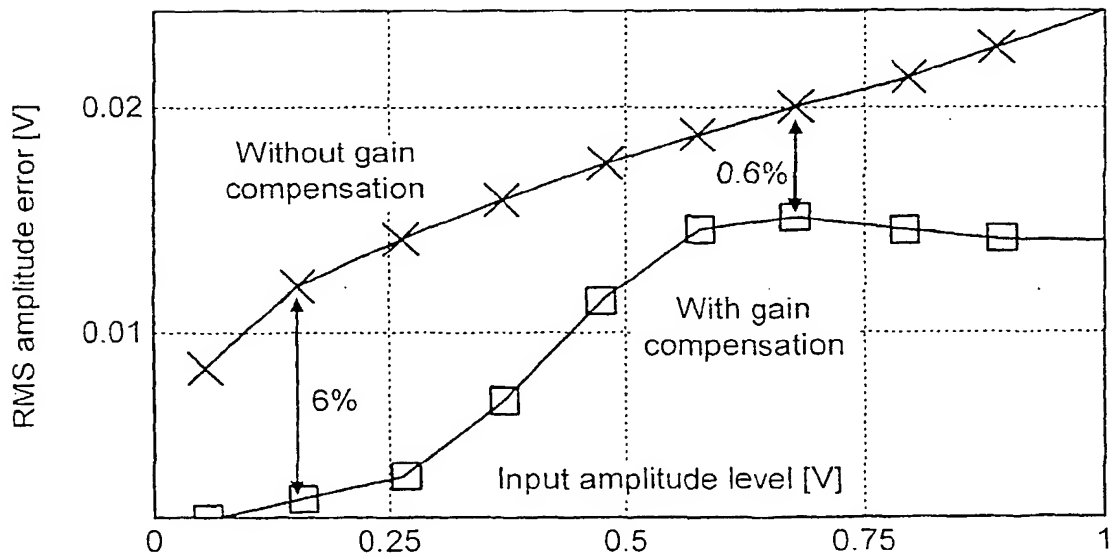


FIG. 4



European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number  
EP 01 31 0323

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
X	WO 00 59172 A (SHIMIZU HIROAKI ; TOKYO SHIBAURA ELECTRIC CO (JP)) 5 October 2000 (2000-10-05)	1-5,7,8, 10,17-19	H04B1/04 H03F1/32 H04L27/36
Y	& EP 1 170 915 A (TOKYO SHIBAURA ELECTRIC CO) 9 January 2002 (2002-01-09) * page 5, line 55 - page 6, line 31 * * page 7, line 21 - page 8, line 28; figures 2-4 *	11-15	
X	WO 00 31881 A (WILSON MARTIN PAUL ; CADENCE DESIGN SYSTEMS INC (US)) 2 June 2000 (2000-06-02) * page 8, line 23 - page 9, line 18; figure 2 *	1-5,7,8, 10,17-19	
Y	EP 0 998 088 A (NOKIA MOBILE PHONES LTD) 3 May 2000 (2000-05-03) * column 4, line 29 - column 5, line 35; figures 1,2 *	11-15	
Y	US 5 430 416 A (BLACK GREGORY R ET AL) 4 July 1995 (1995-07-04) * column 4, line 23 - column 5, line 28; figure 4 *	11-15	TECHNICAL FIELDS SEARCHED (Int.Cl.7)
A	PETROVIC V ET AL: "POLAR-LOOP TRANSMITTER" ELECTRONICS LETTERS, IEE STEVENAGE, GB, vol. 15, no. 10, 10 May 1979 (1979-05-10), pages 286-288, XP000761820 ISSN: 0013-5194 * page 287, left-hand column, line 12 - line 32; figure 1 *	1-5,7,8, 10-15, 17-19	H04B H03F H04L H03C H03G
The present search report has been drawn up for all claims			
Place of search <b>MUNICH</b>		Date of completion of the search <b>29 April 2002</b>	Examiner <b>Lindberg, P</b>
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons &amp; : member of the same patent family, corresponding document</p>			

EPO FORM 1503 03 82 (P04001)

**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 01 31 0323

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on  
The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

29-04-2002

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
WO 0059172 A	05-10-2000	JP 2000286915 A	13-10-2000
		EP 1170915 A1	09-01-2002
		WO 0059172 A1	05-10-2000
		US 2002031191 A1	14-03-2002
WO 0031881 A	02-06-2000	AU 1395300 A	13-06-2000
		DE 19983736 T0	14-03-2002
		GB 2359207 A	15-08-2001
		WO 0031881 A1	02-06-2000
EP 0998088 A	03-05-2000	FI 982329 A	28-04-2000
		EP 0998088 A2	03-05-2000
		JP 2000138723 A	16-05-2000
US 5430416 A	04-07-1995	AU 672549 B2	03-10-1996
		AU 1674995 A	11-09-1995
		CN 1121752 A ,B	01-05-1996
		DE 4480702 C2	20-04-2000
		DE 4480702 T0	21-03-1996
		FR 2716589 A1	25-08-1995
		GB 2291754 A ,B	31-01-1996
		JP 8509589 T	08-10-1996
		RU 2121755 C1	10-11-1998
		SE 9503687 A	21-12-1995
		SG 69952 A1	25-01-2000
		WO 9523453 A1	31-08-1995
		ZA 9500442 A	27-09-1995

EPO FORM P459

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

**THIS PAGE BLANK (USPTO)**